

When Extreme Networking Performance is Mission Critical Mission tested 10G TCP Hardware technology in F35 Fighter Program

Extreme Appliance for: Mission Critical Network and Application Performance

Providing Acceleration Solutions with unprecedented
Performance to Networking Industry since 2010

1K TCP&UDP connections in Full TCP and UDP Offload in massively Parallel
FPGA offers up to 6x higher performance & nano second latency, zero jitter

Extreme Performance Ethernet Acceleration at 10G/40G/50G bps,

Unprecedented Application Acceleration and
Enterprise Class Innovative Solutions from Intilop
In partnership with Intel and Xilinx & IBM

Full TCP and UDP Offload in Massively Parallel FPGA
Technology for Ultra-Low Latency and High Bandwidth

- Increase your work flow by several times due to saved CPU cycles
- Need to Transfer Multi-Giga Bytes (sustained) in a second?
- Need a TCP driver stack that can handle thousands of connections?
- Are multiple Simultaneous TCP or UDP Sessions slowing down your throughput to a crawl and effecting application performance?
- Does transfer jitter get worst in multi-Session scenario?
- Do the TCP & UDP connections have latency in milli or 10s/100s of micro seconds?

Talk to Intilop---

Benefits of Nano-second TCP & UDP Processing

*Largest processing overhead of LAN communication reduced to nano seconds
from micro seconds:*

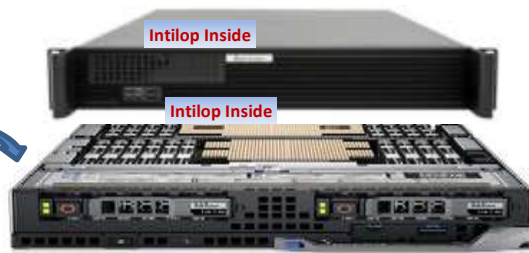
1. **Ultra Highh Throughput** All 1K sessions deliver 10G line rate
2. Greater than **94% Bandwidth** of theoretical limit on 10G Networks.
3. 2 - 16384 Sessions in FPGA hardware deliver same performance
4. Increase the Application performance in Host CPU by up to 500%
5. Decrease the Host CPU power consumption by up to 90%.
6. **100% Deterministic** & Zero Jitter TCP & UDP Processing Results
7. Out of Order UDP fragment processing
8. Most widely adapted Full TCP Offload including ARP Offload. 11 year deployment history and maturity -- e.g. including world's largest stock exchange equipment.
9. HPC with **1.8 Million TCP Protocol** Transactions per second

Other Benefits

- Accelerate your Algorithms in FPGA Hardware by 10x to 50x
- Real-time decision making with 100% determinism
- Decision time in Micro seconds as oppose to Milli seconds or seconds
- Ultra-Precision Pre-Trade Risk management without delaying Order streams
- Mitigate Real-time Micro-Burst stresses on existing Networks. No CPU processing bottlenecks. CPU can do other application processing.
- Use for Large data transfers in data center, cloud servers, web servers, telecom data equipment, military data equipment etc. No need for load balancing or distributing traffic across multiple switch ports.
- Reduce Network equipment footprint & TCO by 70 – 80 %.
- Many custom options available e.g. PCI/NVMe in storage,
- Network Life-Extension and Scalability from 10G to 40G to 100G bps



Available with 2 or 4 Cards, 4 or 16 10G Ports



Available with 1 or 2 4.xx GHz Xeon Class CPUs with up to 256 G Byte DDR4 and 980 GB SSD
Linux Host, Redundant PS, Small form factor, 2U, ½ deep, standard width IBM , HP or white lable servers
Consult intilop for more details

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